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## APPLICANTS

James Schleicher, Santa Clara, CA;

Jim Park, San Jose, CA;

Sergey Shumarayev, San Leandro, CA; Bruce Pedersen, San Jose, CA;

Tony Ngai, Campbell, CA;

Wei-Jen Huang, Burlingame, CA;

Victor Maruri, Mountain View, CA;

Rakesh Patel, Cupertino, CA;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/299,572 11/18/2002 PAT 6,727,727  
which is a CON of 10/017,199 12/14/2001 PAT 6,525,564  
which is a CON of 09/517,146 03/02/2000 PAT 6,366,120  
which claims benefit of 60/122,788 03/04/1999  
and claims benefit of 60/142,431 07/06/1999

*OK Uta*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*None Uta*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/26/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance  Examiner's Signature <i>[Signature]</i> Initials <i>Uta</i>	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 1
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## ADDRESS

36981

FISH &amp; NEAVE LLP

1251 AVENUE OF THE AMERICAS

50TH FLOOR

NEW YORK, NY

10020-1105

## TITLE

Interconnection resources for programmable logic integrated circuit devices